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REQUEST FOR ACCESS TO AN APPLICATION UNDER 37 CFR 1.14(e)

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In re Application of

Application Number

09/620,544

Filed

7/21-2000

Art Unit

Examiner

Paper No. *15*Assistant Commissioner for Patents
Washington, DC 20231

1. I hereby request access under 37 CFR 1.14(e)(2) to the application file record of the above-identified ABANDONED Application, which is not within the file jacket of a pending Continued Prosecution Application (CPA) (37 CFR 1.53(d)) and is: (CHECK ONE)

 (A) referred to in:

United States Patent Application Publication No. _____, page _____, line _____.

United States Patent Number 5,845,313, column _____, line _____, or

an International Application which was filed on or after November 29, 2000 and which

designates the United States, WIPO Pub. No. _____, page _____, line _____.

 (B) referred to in an application that is open to public inspection as set forth in 37 CFR 1.11(b) or 1.14(e)(2)(i), i.e., Application No. _____, paper No. _____, page _____, line _____.

2. I hereby request access under 37 CFR 1.14(e)(1) to an application in which the applicant has filed an authorization to lay open the complete application to the public.

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Unit: _____



US005845313A

#18

United States Patent [19]

Estakhri et al.

[11] **Patent Number:** **5,845,313**
 [45] **Date of Patent:** **Dec. 1, 1998**

[54] **DIRECT LOGICAL BLOCK ADDRESSING
FLASH MEMORY MASS STORAGE
ARCHITECTURE**

62-283497 12/1987 Japan .

[75] **Inventors:** Petro Estakhri, Pleasanton; Mahmud Assar, Morgan Hill, both of Calif.

OTHER PUBLICATIONS

[73] **Assignee:** Lexar, Fremont, Calif.

Steven H. Leibson, "Nonvolatile, In-Circuit-Reprogrammable Memories", *EDN*, Jan. 3, 1993, pp. 89-102.

[21] **Appl. No.:** 509,706

S. Mehroa, J.H. Yuan, R.A. Cernea, W.Y. Chien, "EEPROM for Solid State Disk Applications", 1992 Symposium on VLSI Circuits Digest of Technical Papers, pp. 24-25.

[22] **Filed:** Jul. 31, 1995

T. Nozaki, T. Tanaka, Y. Kijiya, E. Kinoshita, T. Tsuchiya, Y Hayashi, "A 1 MB EEPROM with MONOS Memory Cell for Semiconductor Disk Application", *IEEE Journal of Solid-State Circuits*, vol. 26, No. 4, Apr. 1991, pp. 497-501.

[51] **Int. Cl.⁶** G06F 12/10

Primary Examiner—Eddie P. Chan

[52] **U.S. Cl.** 711/103; 711/104; 711/156;
711/203

Assistant Examiner—Reginald G. Bragdon

[58] **Field of Search** 395/430, 431,
395/435, 483, 413; 711/103, 104, 108,
156, 203

Attorney, Agent, or Firm—Claude A. S. Hamrick;
Oppenheimer W. Donnelly; Maryam Imam

References Cited**[57] ABSTRACT****U.S. PATENT DOCUMENTS**

A nonvolatile semiconductor mass storage system and architecture can be substituted for a rotating hard disk. The system and architecture avoid erase cycles each time information stored in the mass storage is changed. Erase cycle are avoided by programming an altered data file into an empty mass storage block rather than over itself as a hard disk would. Periodically, the mass storage will need to be cleaned up. These advantages are achieved through the use of several flags, and a map to correlate a logical block address of a block to a physical address of that block. In particular, flags are provided for defective blocks, used blocks, and old versions of a block. An array of volatile memory is addressable according to the logical address and stores the physical address

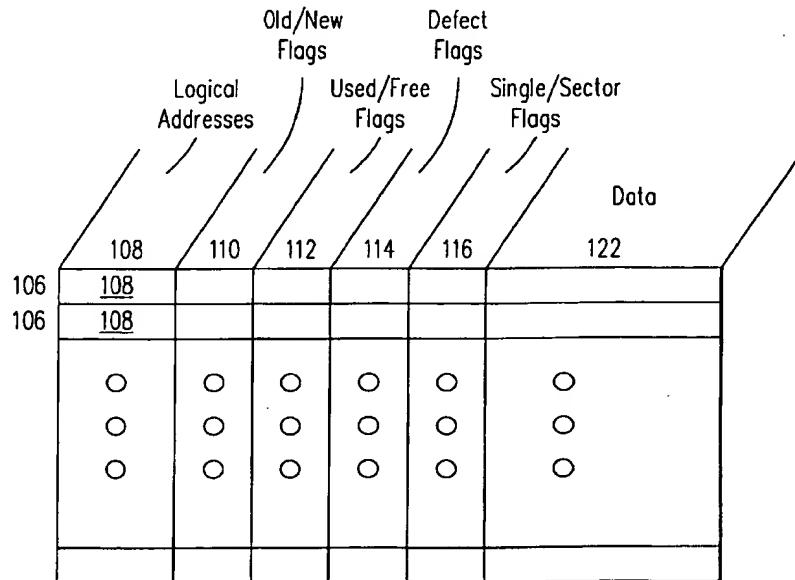
FOREIGN PATENT DOCUMENTS

62-283496 12/1987 Japan .

8 Claims, 5 Drawing Sheets

102	<u>130</u>
102	<u>130</u>
	○
	○
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100



104



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Search results for patent number: 5,845,313

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Parent Continuity Data

Description	Parent Number	Parent Filing Date	Parent Stat
No Parent Continuity Data Found.			

Child Continuity Data

08/831,266 filed on 03-31-1997 which has status of Patented claims the benefit of 0
 08/946,331 filed on 10-07-1997 which has status of Patented claims the benefit of 0
 09/087,720 filed on 05-29-1998 which has status of Patented claims the benefit of 0
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 10/071,972 filed on 02-05-2002 which has status of Pending claims the benefit of 08
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